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	U.S. PATENT DOCUMENTS									
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THE STATE OF THE S		US	5,319,589	06/07/1994	Yamagata et al.					
"UnV		us	5,146,300	09/08/1992	Hamamoto et al	1				
by		US	5,126,968	06/30/1992	Hamamoto et al		J			
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EXAMINER'S		Fo	reign Patent Document	Publication Date	Name of Patentee or	Pages, Colu		Tr	anslation	
INITIALS	CITE	Country Codes -Number 4 -Kind Codes (if known)		MM-DD-YYYY	Applicant of Cited Document	Where Relevant Figures Appear				
	NO.	1	Suges (ii NIOWII)	1			, ,	Yes		lo
		+-	JP 5-298891	11/12/1993		Correspond	Corresponding to U.S.			
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HW		1	IEEE J.S.S.C. vol.27. No.12, Dec.1992 pp. 1927, Tadato Yamagata, et al., "A 288-kb Fully Parallel Content Addressable Memory Using a Stacked-Capacitor Cell Structure"							
HAZ		IEI	IEICE Transactions on Electronics, vol. E76-C, No.11, Nov.1993, pp.1657, Tadato Yamagata, et al. "A Bitline Control Circuit Scheme and Redundancy Technique for High-Density Dynamic Content Addressable Memories"							
LI 2 1	-	IEICE Transactions on Electronics, vol.E77-C, No.8, Aug.1994, pp.1377, Takeshi HAMAMOTO, et al. "A Flexible Search								
DIY	ļ	Managing Circultry for High-Density Dynamic CAMs*								
EXAMINER DATE CONSIDERED										
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1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

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				U.S. PATEN	T DOCUMENTS		2010		
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INITIALS	CITE NO.		nber-Kind Code2 (# known)	MM-DD-YYYY	Name of Patentee or App Document		Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
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EXAMINER'S INITIALS	CITE	Fo	reign Patent Document	Publication Date	Date Name of Patentee or		lumns, Lines	Translation	
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INITIALS	CITE NO.	journa publis	II, serial, symposium, cata hed.	log, etc.), date, page(title of the article (when approp s), volume-issue number(s), pu	oriate), litle o ublisher, city	f the item (boo and/or countr	ok, magazine, y where	
HW	Proceedings of the IEEE 1991, May 12-15, 1991, "CUSTOM INTEGRATED CIRCUITS CONFERENCE", Town & Country Hotel San Diego, California.								—
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 608. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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